



SREE VIDYANIKETHAN ENGINEERING COLLEGE

Sree Sainath Nagar, Tirupati - 517 102

DEPARTMENT OF COMPUTER SCIENCE AND SYSTEMS ENGINEERING

TIME TABLE (2011-2012)

IV B. TECH II Semester

ROOM NO: 114

w.e.f. 11.12.2006

DAY/ HR	10:30-11:20 1	11:20-12:10 2	12:10-1:00 3		2:15-3:05 4	3:05-3:55 5	3:55-4:45 6
MON	OOPS LAB (C & D) (KKC, SSZ, TK, LP)			L U N C H	PC	MPI	CG
TUE	OOPS	MPI	SE		CG	PC	ES
WED	PC	CG	ES		OOPS	SE	MPI
THU	CG	PC	SE		OOPS	MPI	ES
FRI	SE	OOPS	ES		MPI LAB (A & B) (GKR, LP, GP, TK)		
SAT	ES	MPI	CG		PC	SE	OOPS

OOPS	OOPS THROUGH JAVA	Mr. K. KALYAN CHAKRAVARTHY
CG	COMPUTER GRAPHICS	Ms S. SHAZIA
PCE	PRINCIPLES OF COMMUNICATIONS	Ms C. JEEVANA KUMARI
MPI	MICRO-PROCESSORS AND INTERFACING	Mr. G. KANAKA RAJU
ES	ENVIRONMENTAL STUDIES	Mr. M. RAMA CHANDRAIAH
SE	SOFTWARE ENGINEERING	Prof. C. MADHUSUDHANA RAO
OOPS LAB	OOPS LAB	KKC, LP, TK, SSZ
MP LAB	MICROPROCESSORS LAB	GKR, LP, GP,TK

HOD

PRINCIPAL