



SREE VIDYANIKETHAN ENGINEERING COLLEGE

Sree Sainath Nagar, Tirupati – 517 102



TIME TABLE

2011 – 2012

ELECTRICAL & ELECTRONICS ENGINEERING IV – B.TECH. SECTION – B (II SEMESTER)

Room No:106

With effect from: 05-12-2011

Hour	8:50-9:40	9:40-10:30		11:00-11:50	11:50-12:40	12:40-1:30
Day	1	2	B	3	4	5
MON	ACS	ACS	R	UEE	OOP	PROJECT / SEMINAR
TUE	UEE	ACS	E	OOP	OOP	PROJECT / SEMINAR
WED	OOP	UEE	A	UEE	ACS	PROJECT / SEMINAR
THU	UEE	OOP	K	ACS	ACS	PROJECT / SEMINAR
FRI	ACS	OOP		UEE	UEE	PROJECT / SEMINAR
SAT	OOP	OOP		ACS	UEE	PROJECT / SEMINAR

UEE	: Utilization of Electrical Energy	Mr. IKS	: Mr. I. Kumara Swamy
ACS	: Advanced Control Systems	Mr. NS	: Mr. N. Sreekanth
OOP	: Object Oriented Programming	Mr. VLN	: Mr. V. Lokanatham Naidu
PROJECT / SEMINAR	:		

Date: 02-12-2011

HEAD

PRINCIPAL