

SREE VIDYANIKETHAN ENGINEERING COLLEGE

SREE SAINATH NAGAR, TIRUPATI

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

SVEC/CSE/2011-12

Date: 30-11-2011

TIME TABLE ACADEMIC YEAR 2011-2012 III B.Tech - Semester - II - Section B

Room No. 131

w.e.f. 12-12-2011

HOUR	8:00 8:50	8:50 9:40	9:40 10:30	BREAK	11:00 11:50	11:50 12:40	12:40 1:30
DAY					10:30 11:20	11:20 12:10	12:10 1:00
MON	OOAD	AI	OT		UNIX INTERNALS LAB		
TUE	UNIX	OT	AI		OOAD	DS	MPI
WED	OOAD	OT	MPI		DS	UNIX	AI
THU	MPI LAB				MPI	UNIX	OT
FRI	DS	AI	DS		MPI	OOAD	UNIX
SAT	AI	MPI	UNIX		DS	OT	OOAD

9A05601	Object Oriented Analysis and Design	Mr.M.R.PAVAN KUMAR
9A05602	Unix Internals	Mr.N.PAPANNA
9A05603	Optimizing Techniques	Dr.L.V.REDDY
9A04602	Microprocessors and Micro Controllers	Mr.K.BHASKAR NAIK
9A05604	Distributed Systems	Mr.V.RAMESH
9A05605	Artificial Intelligence	Ms.J.DURGA
9A19501	Microprocessors and Interfacing Lab	KBN, SS, ESPK & ASV
9A05606	UNIX Internals Lab	NP, LVR, KSS & PVR

HOD, CSE

PRINCIPAL