

SREE VIDYANIKETHAN ENGINEERING COLLEGE

Sree Sainath Nagar, Tirupati – 517 102

Department of Electronics and Instrumentation Engineering

SVEC/EIE/02/2011-12

03 Dec 2011

TIMETABLE**2011– 12****III B.Tech. II SEMESTER****EIE-B****Room No: 328****w.e.f: 12.12.2010**

HOUR	8.00 - 8.50	8.50 - 9.40	9.40 - 10.30	T E A C H E R S	11.00 - 11.50	11.50 - 12.40	12.40 - 1.30
DAY	I	II	III		IV	V	VI
MON	MP&MC	CO	AI		VLSI	DSP	PCI
TUE	DSP	MP&MC	CO		PCI	AI	VLSI ^(T)
WED	LDICA/ AECS LAB				CO ^(T)	VLSI	DSP ^(T)
THU	AI	CO	PCI		DSP	VLSI	MP&M ^(T) C
FRI	LDICA/ AECS LAB				AI	PCI	MP&MC
SAT	CO	PCI	VLSI		DSP	MP&MC	AI ^(T)

PCI	:	Process Control Instrumentation	:	Prof. N.Gireesh
DSP	:	Digital Signal Processing	:	Mr. M.Saravanan
MP&MC	:	MicroProcessors & Micro Controllers	:	Mr. Y.Dileep Kumar
AI	:	Analytical Instrumentation	:	Ms. N. Harathi
VLSI	:	VLSI Design	:	Mr. K. Cholan
CO	:	Computer Organization	:	Mr. C. Ravindra Moorthy
LDICA LAB	:	Linear and Digital IC Applications Lab	:	Ms. V. Rajini / Mr. C. Ravindra Moorthy
AECS LAB	:	Advanced English communication Skills Lab	:	Mr.C. Ravindra Babu/ Mr.G. Subramanyam
T	:	Tutorial	:	

HOD, EIE**PRINCIPAL**