



SREE VIDYANIKETHAN ENGINEERING COLLEGE

Sree Sainath Nagar, Tirupati – 517 102



TIME TABLE

2011 – 2012

ELECTRICAL & ELECTRONICS ENGINEERING III – B.TECH. SECTION – B (II SEMESTER)

Room No: 208

With effect from: 12-12-2011

Hour	8:00- 8:50	8:50-9:40	9:40-10:30	B	11:00-11:50	11:50-12:40	12:40-1:30
Day	1	2	3		4	5	6
MON	MS	MPMC	LDIC	R	PSA	PSD	PSOC
TUE	AECS / EM LAB			E	MPMC	PSA	LDIC
WED	PSOC	MS	PSA	A	LDIC	MPMC	PSD
THU	LDIC	MPMC	MS	K	PSD	PSOC	PSA
FRI	MS	PSD	PSOC	EM / AECS LAB			
SAT	MPMC	PSOC	PSA		PSD	LDIC	MS

MS	: Management Science	Mr. DRR	: Mr. D. Raghurami Reddy
PSD	: Power Semiconductor Drives	Mr. PR	: Mr. P. Ramesh
PSA	: Power System Analysis	Mr. NMGK	: Mr. N.M.G. Kumar
MPMC	: Microprocessors & Microcontrollers	Ms. PH	: Ms. P. Hymavathi
PSOC	: Power System Operation & Control	Mr. MM	: Mr. M. Manohara
LDIC	: Linear and Digital IC Applications	Mr. GGK	: Mr. G. Ganesh Kumar
AECS LAB	: Advanced English Communication Skills Lab	Mr. CR,	: Mr. C. Raghavendra,
EM LAB	: Electrical Measurements Lab	Ms. GMM	: Ms. G. M. Madhavi
		Mr. NMGK,	: Mr. N.M.G. Kumar,
		Mr. TSK,	: Mr. T. Sunil Kumar,
		Mr. BSR	: Mr. B. Subba Reddy
		:	:

Date: 02-12-2011

HEAD

PRINCIPAL