

# SREE VIDYANIKETHAN ENGINEERING COLLEGE

Sree Sainath Nagar, Tirupati – 517 102

## Department of Electronics and Instrumentation Engineering

SVEC/EIE/02/2011-12

03 Dec 2011

### TIMETABLE

2011– 12

II B.Tech. II SEMESTER

EIE-B

Room No: 317

w.e.f: 26.12.2011

HOUR	10.30 - 11.20	11.20 - 12.10	12.10 - 1.00	L U N C H  B R E A K	2.15 - 3.05	3.05 - 3.55	3.55 - 4.45
DAY	I	II	III		IV	V	VI
MON	EEM	ECA	PEE		PDC	CS	TIN
TUE	AECS (AUDIT)				PEE	CS	EEM <sup>(T)</sup>
WED	ECA / EE LAB				PEE <sup>(T)</sup>	PDC	ECA
THU	CS	EEM	TIN		ECA / EE LAB		
FRI	PDC	PEE	ECA		CS	EEM	TIN
SAT	PEE	EEM	PDC		ECA	TIN	CS <sup>(T)</sup>

ECA	:	Electronic circuit analysis	:	Ms. P. Kavitha
PDC	:	Pulse and digital circuits	:	Mr. M. Balaji
CS	:	Control systems	:	Dr. B. Pollaiah
TIN	:	Transducers in instrumentation	:	Mr. M. Srikanth
EEM	:	Electrical and Electronic Measurements	:	Mr. C. Ravindra Moorthy
PEE	:	Principles of Electrical Engineering	:	Ms. B. Aruna
ECA LAB	:	Electronic circuit analysis Lab	:	Ms. P. Kavitha / Mr. M. Srikanth
EE LAB	:	Electrical Engineering lab	:	Ms. B. Aruna / Mr. D. Sreenivasulu / Mr. N. Pavan Kumar
Audit course	:	Advanced English communications lab	:	Mr. C. Ravindra babu/ Mr. G. Subramanyam
T	:	TUTORIAL		

HOD, EIE

PRINCIPAL