



# SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati – 517 102



## TIME TABLE 2011 – 2012 ELECTRICAL & ELECTRONICS ENGINEERING II EEE – B (II SEMESTER)

Room No: 324 (FN) / 208 (AN)

With effect from: 26-12-2011

Hour	10:30-11:20 1	11:20-12:10 2	12:10-1:00 3		2:15-3:05 4	3:05-3:55 5	3:55-4:45 6
Day							
MON	EMF	EM	NAS	B R E A K	GEP	T&IM	AEC
TUE	GEP	AEC	T&IM		EC&S / SDC LAB		
WED	EM	EMF	T&IM		AECS (AUDIT COURSE)		
THU	SDC / EC&S LAB				NAS	EM	EMF
FRI	T&IM	GEP	AEC		EMF	NAS	EM
SAT	EMF	AEC	EM		GEP	NAS	T&IM

AEC	: Analog Electronic Circuits	Ms. VS	: Ms. V. Sravya
NAS	: Network Analysis & Synthesis	Ms. PH	: MR. P. Hymavathi
EMF	: Electro Magnetic Fields	Mr. AMS	: Mr. A. Muni Sankar
GEP	: Generation of Electric Power	Mr. DSR	: Mr. D. Sreenivasulu Reddy
EM	: Electrical Measurements	Mr. GR	: Mr. G. Ravindra
T&IM	: Transformers & Induction Machines	Mr. TKLR	: Mr. T. Kosaleswara Reddy
SDC LAB	: Semiconductor Devices and Circuits Lab	Mr. TSS,	: Mr. T. Syam Sundar,
EC&S LAB	Electrical Circuits & Simulation Lab	Ms. KS /	: Ms. K. Sudha /
		Ms. VC	: Ms. V. Cherishma
		Ms. PH,	: Ms. P. Hymavathi
		Mr. AMS,	: Mr. A. Muni Sankar
		Mr. GR /	: Mr. G. Ravindra
		Mr. NPKR	: Mr. N. Pavan Kumar Reddy
Audit Course	: Advanced English Communication Skills	Ms. GMM /	: Ms. G. M. Madhavi /
		Mr. GS	: Mr. G. Subramanyam
		:	:

Date: 02-12-2011

HEAD

PRINCIPAL