

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
ANANTAPUR**

B.TECH. ELECTRONICS AND INSTRUMENTATION ENGINEERING

**III YEAR I SEMESTER
COURSE STRUCTURE**

CODE	SUBJECT	T	P	C
	Management Science	4+1*	-	4
	Computer Organization	4+1*	-	4
	Control Systems	4+1*	-	4
	Electronic Circuit Analysis	4+1*	-	4
	Industrial Instrumentation	4+1*	-	4
	Process Control Instrumentation	4+1*	-	4
	Process Control Lab.	-	3	2
	Advanced English Communication Skills Lab	-	3	2
	TOTAL	30	6	28

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B.TECH. ELECTRONICS AND INSTRUMENTATION ENGINEERING

**IV YEAR I SEMESTER
COURSE STRUCTURE**

CODE	SUBJECT	T	P	C
	VLSI Design	4+1*	-	4
	Object Oriented Programming	4+1*	-	4
	Analytical Instrumentation	4+1*	-	4
	P C Based Instrumentation	4+1*	-	4
	ELECTIVE – I	4+1*	-	4
	Power Plant Instrumentation			
	Operating Systems			
	Virtual Instrumentation			
	ELECTIVE – II	4+1*	-	4
	Digital Control Systems			
	Artificial Neural Networks			
	Computer Networks			
	Microprocessors and Interfacing Lab	-	3	2
	Instrumentation Lab - III	-	3	2
	TOTAL	30	6	28

NOTE: All University Examinations (Theory and Practical) are of 3 hours duration.

* : Tutorials

T : Theory periods per week P: Practical /Drawing Periods per week

C : Total Credits for the subject

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III Year B.Tech. EIE I-Sem

T	P	C
4+1*	0	4

MANAGEMENT SCIENCE

Unit I: *Introduction to Management:* Concepts of Management and organization- Nature and Importance of Management, Functions of Management, Taylor's Scientific Management Theory, Fayol's Principles of Management, Maslow's Theory of Human Needs, Douglas McGregor's Theory X and Theory Y, Herzberg's Two-Factor Theory of Motivation, Systems Approach to Management, Leadership Styles, Social responsibilities of Management.

Unit II: *Designing Organisational Structures:* Basic concepts related to Organisation - Departmentation and Decentralisation, Types of mechanistic and organic structures of organisation (Line organization, Line and staff organization, functional organization, Committee organization, matrix organization, Virtual Organisation, Cellular Organisation, team structure, boundaryless organization, inverted pyramid structure, lean and flat organization structure) and their merits, demerits and suitability.

Unit III: *Operations Management:* Principles and Types of Plant Layout-Methods of production (Job, batch and Mass Production), Work Study -Basic procedure involved in Method Study and Work Measurement-Statistical Quality Control: \bar{X} chart, R chart, c chart, p chart, (simple Problems), Acceptance Sampling, Deming's contribution to quality.

Unit IV: A) *Materials Management:* Objectives, Need for Inventory control, EOQ, ABC Analysis, Purchase Procedure, Stores Management and Stores Records - Supply Chain Management
B) *Marketing:* Functions of Marketing, Marketing Mix, Marketing Strategies based on Product Life Cycle., Channels of distribution.

Unit V: *Human Resources Management (HRM):* Concepts of HRM, HRD and Personnel Management and Industrial Relations (PMIR), HRM vs. PMIR, Basic functions of HR Manager: Manpower planning, Recruitment, Selection, Training and Development, Placement, Wage and Salary Administration, Promotion, Transfer, Separation, Performance Appraisal, Grievance Handling and Welfare Administration, Job Evaluation and Merit Rating.

Unit VI: *Project Management (PERT/CPM):* Network Analysis, Programme Evaluation and Review Technique (PERT), Critical Path Method (CPM), Identifying critical path, Probability of Completing the project within given time, Project Cost Analysis, Project Crashing. (simple problems)

Unit VII: *Strategic Management:* Mission, Goals, Objectives, Policy, Strategy, Programmes, Elements of Corporate Planning Process, Environmental Scanning, SWOT Analysis, Steps in Strategy Formulation and Implementation, Generic Strategy alternatives.

Unit VIII: *Contemporary Management Practices:* Basic concepts of Just-In-Time (JIT) System, Total Quality Management (TQM), Six sigma and Capability Maturity Model (CMM) Levels, Value Chain Analysis, Enterprise Resource Planning (ERP), Performance Management, Business Process outsourcing (BPO), Business Process Re-engineering and Bench Marking, Balanced Score Card.

Text Book:

1. Aryasri: *Management Science*, TMH, New Delhi.

Reference Books:

1. Kotler Philip & Keller Kevin Lane: *Marketing Mangement* 12/e, PHI, 2007
2. Koontz & Weihrich: *Essentials of Management*, 6/e, TMH, 2007
3. Thomas N. Duening & John M. Ivancevich *Management—Principles and Guidelines*, Biztantra, 2007.
4. Kanishka Bedi, *Production and Operations Management*, Oxford University Press, 2007.
5. Memoria & S.V. Ganker, *Personnel Management*, Himalaya, 25/e, 2007

6. Schermerhorn: *Management*, Wiley, 2007.
7. Parnell: *Strategic Management*, Biztantra, 2007.
8. L.S.Srinath: *PERT/CPM*, Affiliated East-West Press, 2007.

Pre-requisites: Managerial Economics

Objective: To familiarize with the process of management and to provide basic insights into select contemporary management practices.

Codes/Tables: Normal Distribution Function Table need to be permitted into the examination Hall.

Question Paper Pattern: 5 Questions to be answered out of 8 questions.
Each question should not have more than 3 bits.
Unit VIII will have only short questions, not essay questions.

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III Year B.Tech. EIE I-Sem

T	P	C
4+1*	0	4

COMPUTER ORGANIZATION

UNIT I :

BASIC STRUCTURE OF COMPUTERS : Computer Types, Functional unit, Basic OPERATIONAL concepts, Bus structures, Software, Performance, multiprocessors and multi computers. Data Representation. Fixed Point Representation. Floating – Point Representation. Error Detection codes.

UNIT II :

REGISTER TRANSFER LANGUAGE AND MICROOPERATIONS : Register Transfer language. Register Transfer Bus and memory transfers, Arithmetic Microoperations, logic micro operations, shift micro operations, Arithmetic logic shift unit. Instruction codes. Computer Registers Computer instructions – Instruction cycle.

Memory – Reference Instructions. Input – Output and Interrupt. STACK organization. Instruction formats. Addressing modes. DATA Transfer and manipulation. Program control. Reduced Instruction set computer.

UNIT III :

MICRO PROGRAMMED CONTROL : Control memory, Address sequencing, microprogram example, design of control unit Hard wired control. Microprogrammed control

UNIT IV :

COMPUTER ARITHMETIC : Addition and subtraction, multiplication Algorithms, Division Algorithms, Floating – point Arithmetic operations. Decimal Arithmetic unit Decimal Arithmetic operations.

UNIT V :

THE MEMORY SYSTEM : Basic concepts semiconductor RAM memories. Read-only memories Cache memories performance considerations, Virtual memories secondary storage. Introduction to RAID.

UNIT-VI

INPUT-OUTPUT ORGANIZATION : Peripheral Devices, Input-Output Interface, Asynchronous data transfer Modes of Transfer, Priority Interrupt Direct memory Access, Input –Output Processor (IOP) Serial communication; Introduction to peripheral component, Interconnect (PCI) bus. Introduction to standard serial communication protocols like RS232, USB, IEEE1394.

UNIT VII :

PIPELINE AND VECTOR PROCESSING : Parallel Processing, Pipelining, Arithmetic Pipeline, Instruction Pipeline, RISC Pipeline Vector Processing, Array Processors.

UNIT VIII :

MULTI PROCESSORS : Characteristics or Multiprocessors, Interconnection Structures, Interprocessor Arbitration. InterProcessor Communication and Synchronization Cache Coherence. Shared Memory Multiprocessors.

TEXT BOOKS :

1. Computer Organization – Carl Hamacher, Zvonks Vranesic, SafeaZaky, Vth Edition, McGraw Hill.
2. Computer Systems Architecture – M.Moris Mano, Illrd Edition, Pearson/PHI

REFERENCES :

1. Computer Organization and Architecture – William Stallings Sixth Edition, Pearson/PHI
2. Structured Computer Organization – Andrew S. Tanenbaum, 4th Edition PHI/Pearson
3. Fundamentals or Computer Organization and Design, - Sivaraama Dandamudi Springer Int. Edition.
4. Computer Architecture a quantitative approach, John L. Hennessy and David A. Patterson, Fourth Edition Elsevier
5. Computer Architecture: Fundamentals and principles of Computer Design, Joseph D. Dumas II, BS Publication.

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CONTROL SYSTEMS

Objective :

In this course it is aimed to introduce to the students the principles and applications of control systems in every day life. The basic concepts of block diagram reduction, time domain analysis solutions to time invariant systems and also deals with the different aspects of stability analysis of systems in frequency domain and time domain.

UNIT – I INTRODUCTION

Concepts of Control Systems- Open Loop and closed loop control systems and their differences- Different examples of control systems- Classification of control systems, Feed-Back Characteristics, Effects of feedback.

Mathematical models – Differential equations, Impulse Response and transfer functions - Translational and Rotational mechanical systems

UNIT II TRANSFER FUNCTION REPRESENTATION

Transfer Function of DC Servo motor - AC Servo motor- Synchro transmitter and Receiver, Block diagram representation of systems considering electrical systems as examples -Block diagram algebra – Representation by Signal flow graph - Reduction using Mason's gain formula.

UNIT-III TIME RESPONSE ANALYSIS

Standard test signals - Time response of first order systems – Characteristic Equation of Feedback control systems, Transient response of second order systems - Time domain specifications – Steady state response - Steady state errors and error constants – Effects of proportional derivative, proportional integral systems.

UNIT – IV STABILITY ANALYSIS IN S-DOMAIN

The concept of stability – Routh's stability criterion – qualitative stability and conditional stability – limitations of Routh's stability

Root Locus Technique:

The root locus concept - construction of root loci-effects of adding poles and zeros to $G(s)H(s)$ on the root loci.

UNIT – V FREQUENCY RESPONSE ANALYSIS

Introduction, Frequency domain specifications-Bode diagrams-Determination of Frequency domain specifications and transfer function from the Bode Diagram-Phase margin and Gain margin-Stability Analysis from Bode Plots.

UNIT – VI STABILITY ANALYSIS IN FREQUENCY DOMAIN

Polar Plots-Nyquist Plots-Stability Analysis.

UNIT – VII CLASSICAL CONTROL DESIGN TECHNIQUES

Compensation techniques – Lag, Lead, Lead-Lag Controllers design in frequency Domain, PID Controllers.

UNIT – VIII State Space Analysis of Continuous Systems

Concepts of state, state variables and state model, derivation of state models from block diagrams, Diagonalization- Solving the Time invariant state Equations- State Transition Matrix and it's Properties – Concepts of Controllability and Observability

TEXT BOOKS:

1. Automatic Control Systems 8th edition– by B. C. Kuo 2003– John wiley and son's.,
2. Control Systems Engineering – by I. J. Nagrath and M. Gopal, New Age International (P) Limited, Publishers, 2nd edition.

REFERENCE BOOKS:

1. Modern Control Engineering – by Katsuhiko Ogata – Prentice Hall of India Pvt. Ltd., 3rd edition, 1998.
2. Control Systems by N.K.Sinha, New Age International (P) Limited Publishers, 3rd Edition, 1998.
3. Control Systems Engg. by NISE 3rd Edition – John wiley
4. " Modelling & Control Of Dynamic Systems" by Narciso F. Macia George J. Thaler, Thomson Publishers.

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ELECTRONIC CIRCUIT ANALYSIS

UNIT I : SINGLE STAGE AMPLIFIERS : Review, Small Signal Analysis of Junction Transistor, Frequency response of Common Emitter Amplifier, Common Base Amplifier, Common Collector Amplifier, JFET Amplifiers, Common Drain (CD) Amplifier, Common Gate Amplifier, Gain Band Width Product.

UNIT II : MULTI STAGE AMPLIFIERS : Multi Stage Amplifiers Methods of Inter Stage Coupling, n – Stage Cascaded Amplifier, Equivalent Circuits, Miller’s Theorem, Frequency Effects, Amplifier Analysis, High Input Resistance Transistor Circuits. Cascode – Transistor Configuration, CE-CC Amplifiers, Two Stage RC Coupled JFET amplifier (in Common Source (CS) configuration), Difference Amplifier.

UNIT III : HIGH FREQUENCY TRANSISTOR CIRCUITS : Transistor at High Frequencies, Hybrid- π Common Emitter Transconductance Model, Determination of Hybrid- π Conductances, Variation of Hybrid Parameters with $|I_C|$, $|V_{CE}|$ and Temperature. The Parameters β_t , expression for β_b , Current Gain with Resistance Load, CE Short Circuit Current Gain, Hybrid- π Parameters, Measurement of β_t , Variation of Hybrid – π Parameters with Voltage, Current and Temperature, Design of High frequency Amplifier.

UNIT IV : POWER AMPLIFIERS : Class A Power Amplifier, Maximum Value of Efficiency of Class A Amplifier, Transformer Coupled Amplifier, Transformer Coupled Audio Amplifier, Push Pull Amplifier, Complimentary Symmetry Circuits (Transformer Less Class B Power Amplifier), Phase Inverters, Class D Operation, Class S Operation, Heat Sinks.

UNIT V : TUNED AMPLIFIERS - I : Single Tuned Capacitive Coupled Amplifier, Tapped Single Tuned Capacitance Coupled Amplifier, Single Tuned Transformer Coupled or Inductively Coupled Amplifier, CE Double Tuned Amplifier, Application of Tuned Amplifiers.

UNIT VI : TUNED AMPLIFIERS - II : Stagger Tuning, Stability Considerations, Tuned Class B and Class C Amplifiers, Wideband Amplifiers, Tuned Amplifiers.

UNIT VII : VOLTAGE REGULATORS : Terminology, Basic Regulator Circuit, Short Circuit Protection, Current Limiting, Specifications of Voltage Regulator Circuits, Voltage Multipliers.

UNIT VIII : SWITCHING AND IC VOLTAGE REGULATORS : IC 723 Voltage Regulators and Three Terminal IC regulators, DC to DC Converter, Switching Regulators, Voltage Multipliers, UPS, SMPS.

TEXT BOOKS :

1. Integrated Electronics – J. Millman and C.C. Halkias, Mc Graw-Hill, 1972.
2. Electronic Devices and Circuits, Theodore F. Bogart Jr., J.S. Beasley and G. Rico, Pearson Edition, 6th Edition, 2004.

REFERENCES :

1. Electronic Devices and Circuits Theory – Robert L. Boylestad and Louis Nashelsky, Pearson/Prentice Hall, 9th Edition, 2006.
2. Micro Electronic Circuits – Sedra A.S. and K.C. Smith, Oxford University Press, 5th ed.
3. Micro Electronic Circuits: Analysis and Design – M.H. Rashid, Thomson PWS Publ., 1999.
4. Principles of Electronic Circuits – S.G. Burns and P.R. Bond, Galgotia Publications, 2nd Edn., 1998.
5. Electronic Circuit Analysis and Design – Donald A. Neaman, Mc Graw Hill.
6. Electronic Circuit Analysis – K. Lal Kishore, BS Publications, 2004.

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INDUSTRIAL INSTRUMENTATION

UNIT – I: METROLOGY

Measurement of length – Plainness – Area – Diameter – Roughness – Angle – Comparators – Gauge blocks – Optical Methods of length and distance measurements.

UNIT – II: VELOCITY AND ACCELERATION MEASUREMENT

Relative velocity – Translational and Rotational velocity measurement – Revolution counters and Timers - Magnetic and Photoelectric pulse counting stroboscopic methods - Accelerometers of different types - Gyroscopes.

UNIT – III: FORCE AND TORQUE MEASUREMENT

Force measurement – Different methods –Torque measurement – Dynamometers- Gyroscopic Force and Torque Measurement – Vibrating wire Force transducer

UNIT – IV: PRESSURE MEASUREMENT

Basics of Pressure measurement – Deadweight Gages and Manometers types – Force-Balance and Vibrating Cylinder Transducers – High and Low Pressure measurement – McLeod Gage, Knudsen Gage, Momentum Transfer Gages, Thermal Conductivity Gages, Ionization Gages, Dual Gage Techniques.

UNIT – V: FLOW MEASUREMENT

Head type, Area type (Rota meter), electromagnetic type, Positive displacement type, mass flow meter, ultrasonic type, vortex shedding type, Hotwire anemometer type.. Laser Doppler Veloci-meter.

UNIT – VI: DENSITY MEASUREMENT

Volume Flow meter Plus Density measurement – Strain Gauge load cell method – Buoyancy method - Air pressure balance method – Gamma ray method – Vibrating probe method. Direct Mass Flow meters.

UNIT – VII: RADIATION MEASUREMENT

Radiation Fundamentals. Radiation Detectors. Radiation Thermometers. Optical Pyrometers.

UNIT – VIII: OTHER MEASUREMENTS

Sound-Level Meter. Microphones. Time, Frequency, and Phase-Angle measurement. Liquid Level. Humidity. Chemical Composition. Particle Instruments and Clean-Room

TEXT BOOKS:

1. Measurement Systems – Applications and Design – by Doebelin E.O., 4/e, McGraw Hill International, 1990.
2. Principles of Industrial Instrumentation – Patranabis D. TMH. End edition 1997

REFERENCES:

1. Process Instruments and Control Handbook – by Considine D.M., 4/e, McGraw Hill International, 1993.
2. Mechanical and Industrial Measurements – by Jain R.K., Khanna Publishers, 1986.
3. Instrument Technology, vol. I – by Jones E.B., Butterworths, 1981.

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PROCESS CONTROL INSTRUMENTATION

UNIT – I:

PROCESS DYNAMICS

Process variables – Load variables – Dynamics of simple pressure, flow level and temperature process – interacting and non-interacting systems – continuous and batch process – self-regulation – Servo and Regulator operation - problems.

UNIT – II:

CONTROL ACTIONS AND CONTROLLERS

Basic control actions – characteristics of two position, three position, Proportional, Single speed floating, Integral and Derivative control modes – PI, PD, PID control modes – Problems –

UNIT – III:

TYPES OF CONTROLLERS

Pneumatic, Hydraulic and Electronic Controllers to realize various control actions.

UNIT – IV:

CONTROLLER SETTINGS

Evaluation criteria – 1/4th decay ratio, IEA, ISE, ITAE - determination of optimum settings for mathematically described process using time response and frequency response.

UNIT – V:

TUNING OF CONTROLLERS

Tuning process curve reaction method – continuous oscillation method – damped oscillation method – problems.

UNIT – VI:

FINAL CONTROL ELEMENTS

I/P Converter , P/I converter - pneumatic, electric and hydraulic actuators – valve positioner

UNIT – VII:

CONTROL VALVES

Control valves – characteristic of control valves – valve body – Globe, Butterfly, diaphragm, Ball valves – Control valve sizing – Cavitations, flashing - problems.

UNIT – VIII:

MULTILOOP CONTROL SYSTEM

Feed forward control – Ratio control – Cascade control – Split range – Multivariable control and examples from distillation column and Boiler system.

TEXT BOOKS :

1. Chemical Process Control : An introduction to Theory and Practice – by Stephanopoulos, Prentice Hall, New Delhi, 1999.
2. Process Control – Harriott P. , TMH, 1991

REFERENCES:

1. Process Control, Third Edition – Liptak B.G., Chilton Book Company, Pennsylvania, 1995
2. Process control – by Pollard A., Heinemann Educational Books, London, 1971.
3. Automatic Process Control – by Eckman D.P. , Wiley Eastern Ltd., New Delhi, 1993.
4. Process Control – by Patranabis.
5. Process System Analysis and Control – Coughanowr, McGraw Hill, Singapore, 1991

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T	P	C
0	3	2

PROCESS CONTROL LAB

(Minimum 10 experiments should be conducted)

1. Flow level control unit.
2. Temperature level control unit.
3. Servo and regulator operation.
4. Realization of control actions: Pneumatic controllers. Hydraulic controllers.
5. Electronic controllers.
6. Process tuning – Process reaction curve method.
7. Process tuning – continuous and damped oscillation method.
8. Operation of flow loop in plant.
9. Input convertor – Pneumatic actuator.
10. Input convertor – Hydraulic actuator.
11. Control valve characteristics (Different types).
12. Multi loop control systems – Ratio Control.
13. Multi loop control systems – Cascade Control.

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T	P	C
0	3	2

ADVANCED ENGLISH COMMUNICATION SKILLS LAB

1. Introduction

The introduction of the English Language Lab is considered essential at 3rd year level. At this stage the students need to prepare themselves for their careers which may require them to listen to, read, speak and write in English both for their professional and interpersonal communication in the globalised context.

The proposed course should be an integrated theory and lab course to enable students to use 'good' English and perform the following:

- Gather ideas and information, to organise ideas relevantly and coherently.
- Engage in debates.
- Participate in group discussions.
- Face interviews.
- Write project/research reports/technical reports.
- Make oral presentations.
- Write formal letters.
- Transfer information from non-verbal to verbal texts and vice versa.
- To take part in social and professional communication.

2. Objectives:

This Lab focuses on using computer-aided multimedia instruction for language development to meet the following targets:

- To improve the students' fluency in English, through a well-developed vocabulary and enable them to listen to English spoken at normal conversational speed by educated English speakers and respond appropriately in different socio-cultural and professional contexts.
- Further, they would be required to communicate their ideas relevantly and coherently in writing.

3. Syllabus:

The following course content is prescribed for the Advanced Communication Skills Lab:

- Functional English - starting a conversation – responding appropriately and relevantly – using the right body language – role play in different situations.
- Vocabulary building – synonyms and antonyms, word roots, one-word substitutes, prefixes and suffixes, study of word origin, analogy, idioms and phrases.
- Group Discussion – dynamics of group discussion, intervention, summarizing, modulation of voice, body language, relevance, fluency and coherence.
- Interview Skills – concept and process, pre-interview planning, opening strategies, answering strategies, interview through tele and video-conferencing.
- Resume' writing – structure and presentation, planning, defining the career objective, projecting ones strengths and skill-sets, summary, formats and styles, letter-writing.
- Reading comprehension – reading for facts, guessing meanings from context, scanning, skimming, inferring meaning, critical reading.
- Technical Report writing – Types of formats and styles, subject matter – organization, clarity, coherence and style, planning, data-collection, tools, analysis.

4. Minimum Requirement:

The English Language Lab shall have two parts:

- i) **The Computer aided Language Lab** for 60 students with 60 systems, one master console, LAN facility and English language software for self- study by learners.
- ii) **The Communication Skills Lab** with movable chairs and audio-visual aids with a P.A System, a T. V., a digital stereo –audio & video system and camcorder etc.

System Requirement (Hardware component):

Computer network with Lan with minimum 60 multimedia systems with the following specifications:

- i) P – IV Processor
 - a) Speed – 2.8 GHZ
 - b) RAM – 512 MB Minimum
 - c) Hard Disk – 80 GB
- ii) Headphones of High quality

5. Suggested Software:

The software consisting of the prescribed topics elaborated above should be procured and used.

Suggested Software:

- **Clarity Pronunciation Power** – part II
- **Oxford Advanced Learner's Compass**, 7th Edition
- **DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice.**
- **Lingua TOEFL CBT Insider**, by Dreamtech
- **TOEFL & GRE(KAPLAN, AARCO & BARRONS, USA, Cracking GRE by CLIFFS)**
- **The following software from 'train2success.com'**
 - **Preparing for being Interviewed,**
 - **Positive Thinking,**
 - **Interviewing Skills,**
 - **Telephone Skills,**
 - **Time Management**
 - **Team Building,**
 - **Decision making**

- **English in Mind**, Herbert Puchta and Jeff Stranks with Meredith Levy, Cambridge

6. Books Recommended:

1. **Effective Technical Communication**, M. Ashraf Rizvi, Tata Mc. Graw-Hill Publishing Company Ltd.
2. **A Course in English communication** by Madhavi Apte, Prentice-Hall of India, 2007.
3. **Communication Skills** by Leena Sen, Prentice-Hall of India, 2005.
4. **Academic Writing- A Practical guide for students** by Stephen Bailey, Rontledge Falmer, London & New York, 2004.
5. **English Language Communication : A Reader cum Lab Manual** Dr A Ramakrishna Rao, Dr G Natanam & Prof SA Sankaranarayanan, Anuradha Publications, Chennai
6. **Body Language- Your Success Mantra** by Dr. Shalini Verma, S. Chand, 2006.
7. **DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice**, New Age International (P) Ltd., Publishers, New Delhi.
8. Books on **TOEFL/GRE/GMAT/CAT** by Barron's/cup
9. **IELTS series with CDs** by Cambridge University Press.
10. **Technical Report Writing Today** by Daniel G. Riordan & Steven E. Pauley, Biztantra Publishers, 2005.
11. **Basic Communication Skills for Technology** by Andra J. Rutherford, 2nd Edition, Pearson Education, 2007.
12. **Communication Skills for Engineers** by Sunita Mishra & C. Muralikrishna, Pearson Education, 2007.
13. **Objective English** by Edgar Thorpe & Showick Thorpe, 2nd edition, Pearson Education, 2007.
14. **Cambridge Preparation for the TOEFL Test** by Jolene Gear & Robert Gear, 4th Edition.
15. **Technical Communication** by Meenakshi Raman & Sangeeta Sharma, Oxford University Press.

DISTRIBUTION AND WEIGHTAGE OF MARKS:

Advanced Communication Skills Lab Practicals:

1. The practical examinations for the English Language Laboratory practice shall be conducted as per the University norms prescribed for the core engineering practical sessions.
2. For the English Language lab sessions, there shall be a continuous evaluation during the year for 25 sessional marks and 50 End Examination marks. Of the 25 marks, 15 marks shall be awarded for day-to-day work and 10 marks to be awarded by conducting Internal Lab Test(s). The End Examination shall be conducted by the teacher concerned with the help of another member of the staff of the same department of the same institution.

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VLSI DESIGN

UNIT I

INTRODUCTION

Introduction to IC Technology – MOS, PMOS, NMOS, CMOS & BiCMOS technologies- Oxidation, Lithography, Diffusion, Ion implantation, Metallisation, Encapsulation, Probe testing, Integrated Resistors and Capacitors.

UNIT II

BASIC ELECTRICAL PROPERTIES

Basic Electrical Properties of MOS and BiCMOS Circuits: I_{ds} - V_{ds} relationships, MOS, transistor threshold Voltage, g_m , g_{ds} , figure of merit Co ; Pass transistor, NMOS Inverter, Various pull ups, CMOS Inverter analysis and design, Bi-CMOS Inverters.

UNIT III

VLSI CIRCUIT DESIGN PROCESSES

VLSI Design Flow, MOS Layers, Stick Diagrams, Design Rules and Layout, 2:m CMOS Design rules for wires, Contacts and Transistors Layout Diagrams for NMOS and CMOS Inverters and Gates, Scaling of MOS circuits, Limitations of Scaling.

UNIT IV

GATE LEVEL DESIGN

Logic Gates and Other complex gates, Switch logic, Alternate gate circuits, Basic circuit concepts, Sheet Resistance R_s and its concept to MOS, Area Capacitance Units, Calculations - t - Delays, Driving large Capacitive Loads, Wiring Capacitances, Fan-in and fan-out, Choice of layers

UNIT V

SUBSYSTEM DESIGN

Subsystem Design, Shifters, Adders, ALUs, Multipliers, Parity generators, Comparators, Zero/One Detectors, Counters, High Density Memory Elements.

UNIT VI

SEMICONDUCTOR INTEGRATED CIRCUIT DESIGN

PLAs, FPGAs, CPLDs, Standard Cells, Programmable Array Logic, Design Approach.

UNIT VII

VHDL SYNTHESIS

VHDL Synthesis, Circuit Design Flow, Circuit Synthesis, Simulation, Layout, Design capture tools, Design Verification Tools, Test Principles.

UNIT VIII

CMOS TESTING

CMOS Testing, Need for testing, Test Principles, Design Strategies for test, Chiplevel Test Techniques, System-level Test Techniques, Layout Design for improved Testability.

TEXTBOOKS

1. Essentials of VLSI circuits and systems – Kamran Eshraghian, Eshraghian Douglas and A. Pucknell, PHI, 2005 Edition.
2. Principles of CMOS VLSI Design - Weste and Eshraghian, Pearson Education, 1999.

REFERENCES

1. Chip Design for Submicron VLSI: CMOS Layout & Simulation, - John P. Uyemura, Thomson Learning.
2. Introduction to VLSI Circuits and Systems - John .P. Uyemura, JohnWiley, 2003.
3. Digital Integrated Circuits - John M. Rabaey, PHI, EEE, 1997.
4. Modern VLSI Design - Wayne Wolf, Pearson Education, 3rd Edition, 1997.

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OBJECT ORIENTED PROGRAMMING

UNIT I :

Object oriented thinking :- Need for oop paradigm, A way of viewing world – Agents, responsibility, messages, methods, classes and instances, class hierarchies (Inheritance), method binding, overriding and exceptions, summary of oop concepts, coping with complexity, abstraction mechanisms.

UNIT II :

Java Basics History of Java, Java buzzwords, datatypes, variables, scope and life time of variables, arrays, operators, expressions, control statements, type conversion and costing, simple java program, classes and objects – concepts of classes, objects, constructors, methods, access control, this keyword, garbage collection, overloading methods and constructors, parameter passing, recursion, string handling.

UNIT III :

Inheritance – Hierarchical abstractions, Base class object, subclass, subtype, substitutability, forms of inheritance- specialization, specification, construction, extension, limitation, combination, benefits of inheritance, costs of inheritance. Member access rules, super uses, using final with inheritance, polymorphism- method overriding, abstract classes.

UNIT IV :

Packages and Interfaces : Defining, Creating and Accessing a Package, Understanding CLASSPATH, importing packages, differences between classes and interfaces, defining an interface, implementing interface, applying interfaces, variables in interface and extending interfaces.
Exploring packages – Java.io, java.util.

UNIT V :

Exception handling and multithreading - Concepts of exception handling, benefits of exception handling, Termination or resumptive models, exception hierarchy, usage of try, catch, throw, throws and finally, built in exceptions, creating own exception sub classes. Differences between multi threading and multitasking, thread life cycle, creating threads, synchronizing threads, daemon threads, thread groups.

UNITVI :

Event Handling : Events, Event sources, Event classes, Event Listeners, Delegation event model, handling mouse and keyboard events, Adapter classes, inner classes. The AWT class hierarchy, user interface components- labels, button, canvas, scrollbars, text components, check box, check box groups, choices, lists panels – scrollpane, dialogs, menubar, graphics, layout manager – layout manager types – boarder, grid, flow, card and grib bag.

UNIT VII :

Applets – Concepts of Applets, differences between applets and applications, life cycle of an applet, types of applets, creating applets, passing parameters to applets.

Swing – Introduction, limitations of AWT, MVC architecture, components, containers, exploring swing- JApplet, JFrame and JComponent, Icons and Labels, text fields, buttons – The JButton class, Check boxes, Radio buttons, Combo boxes, Tabbed Panes, Scroll Panes, Trees, and Tables.

UNIT VIII :

Networking – Basics of network programming, addresses, ports, sockets, simple client server program, multiple clients, Java .net package

Packages – java.util,

TEXT BOOKS :

1. Java; the complete reference, 7th editon, Herbert schildt, TMH.
2. Understanding OOP with Java, updated edition, T. Budd, pearson eduction.

REFERENCES :

1. An Introduction to programming and OO design using Java, J.Nino and F.A. Hosch, John wiley & sons.
2. An Introduction to OOP, second edition, T. Budd, pearson education.
3. Introduction to Java programming 6th edition, Y. Daniel Liang, pearson education.
4. An introduction to Java programming and object oriented application development, R.A. Johnson-Thomson.
5. Core Java 2, Vol 1, Fundamentals, Cay.S.Horstmann and Gary Cornell, seventh Edition, Pearson Education.
6. Core Java 2, Vol 2, Advanced Features, Cay.S.Horstmann and Gary Cornell, Seventh Edition, Pearson Education
7. Object Oriented Programming through Java, P. Radha Krishna, University Press.

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ANALYTICAL INSTRUMENTATION

UNIT – I: pH AND CONDUCTIVITY & DISSOLVED COMPONENT ANALYSER

Conductivity meters – pH meters – Dissolved oxygen, hydrogen analyzers – Sodium analyzer – Silica analyzer and sampling systems.

UNIT – II: GAS ANALYSERS

Thermal conductivity types – CO monitor – NOX analyzer – H₂S analyzer system and sampling – Industrial analyzer circuits, Theory and problems on Beer – Lamberts Law.

UNIT – III: CHROMATOGRAPHY - I

Gas chromatography – Liquid chromatography – their principles and applications –

UNIT – IV: CHROMATOGRAPHY - II

oxygen analyzer – paramagnetic type – detectors and sampling systems.

UNIT – V: SPECTROPHOTOMETERS - I

UV, VIS Spectrophotometers – Single beam and double beam instruments – Instrumentation associated with the above Spectrophotometers – Sources and detectors – Sources and detectors for IR Spectrophotometers.

UNIT – VI: SPECTROPHOTOMETERS - II

FT IR Spectrometer – Flame emission and atomic absorption Spectrophotometer – Atomic emission Spectrophotometer - sources for Flame Photometers and online calorific value measurements.

UNIT – VII: PRINCIPLE OF NUCLEAR MAGNETIC RESONANCE

Instrumentation associated with NMR Spectrophotometer – Introduction to mass spectrophotometers , Principle and brief discussion on ELECTRON SPIN RESONANCE (ESR.)

UNIT – VIII: APPLICATIONS

Nuclear radiation detectors – Ionization chamber – GM Counter – Proportional Counter – Solid state detectors.

TEXT BOOK:

1. Handbook of Analytical Instruments – by Khandpur. TMH

REFERENCES:

1. Instrumental Methods of Analysis – by Willard H.H., Merrit L.L., Dean J.A.. and Seattle F.L., CBS Publishing and Distributors, 6/e, 1995.
2. Instrument Technology – by Jones B.E., Butterworth Scientific Publ., London, 1987.
3. Mechanical and Industrial Measurements – by Jain R.K., Khanna Publishing, New Delhi, 2/e, 1992.
4. Principles of Instrumental Analysis – by Skoog D.A. and West D.M., Holt Sounder Publication, Philadelphia, 1985.
5. Instrumental Analysis – by Mann C.K., Vickerks T.J. & Gullick W.H., Harper and Row Publishers, New York, 1974.

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PC BASED INSTRUMENTATION

UNIT – I : Introduction to Computers : Personal Computer, Operating System, I/O Ports, Plug-in-slots, PCI bus, Operators Interface. Computer Interfacing for Data Acquisition and Control – Interfacing Input Signals, Output system with continuous actuators.

Unit – II: Data Acquisition and Control using Standard Cards: PC expansion systems, Plug-in Data Acquisition Boards; Transducer to Control room, Backplane bus – VXI

Unit – III: PC Programming Considerations Using the command line interface; Assembly language programming; C and C++ programming; Data transfer; Scaling and linearization;

UNIT – IV: Programmable logic controller (PLC) basics: Definition, overview of PLC systems, input/output modules, power supplies and isolators.

UNIT – V: Basic PLC programming Programming on-off inputs/ outputs. Creating Ladder diagrams Basic PLC functions PLC Basic Functions, register basics, timer functions, counter functions.

UNIT – VI: PLC intermediate functions: Arithmetic functions, number comparison functions, Skip and MCR functions, data move systems. Utilizing digital bits, sequencer functions, matrix functions.

UNIT – VII: PLC Advanced functions: Analog PLC operation, networking of PLC, PLC-PID functions.

UNIT – VIII: Related Topics

Alternate programming languages. Auxiliary commands and functions. PLC installation, troubleshooting and maintenance.

Field bus: Introduction, concept. HART protocol: Method of operation, structure, and applications. Smart transmitters, smart valves and smart actuators.

TEXT BOOKS:

1. John. W .Webb Ronald A Reis , Programmable Logic Controllers - Principles and Applications, Fourth edition, Prentice Hall Inc., New Jersey, 1998.
2. Computer Control of Processes – M.Chidambaram. Narosa 2003

REFERENCES:

1. PC Based Instrumentation and Control Third Edition by Mike Tooley ; Elsevier
2. PC Interfacing and Data Acquisition Techniques for Measurement, Instrumentation and Control. By Kevin James; Elsevier
3. Practical Data Acquisition for Instrumentation and Control Systems by John Park and Steve Mackay
4. Distributed Control Systems, Lukcas M.P, Van Nostrand Reinhold Co., New York, 1986.
5. Programmable Logic Controllers, Second edition, Frank D. Petruzella, McGraw Hill, Newyork, 1997.

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**POWER PLANT INSTRUMENTATION
(ELECTIVE - I)**

UNIT – I: AN OVERVIEW OF POWER GENERATION

Brief survey of methods of power generation – Hydrothermal, Nuclear, Solar, Wind etc. Importance of instrumentation for power generation – Thermal power plants – Building blocks – Details of the Boiler Processes – PI diagram of Boiler – Cogeneration.

UNIT – II: PARAMETERS AND MEASUREMENTS - I

Electrical measurements – current, Voltage, Power, Frequency power factor, Trivector meter –

UNIT – III: PARAMETERS AND MEASUREMENTS - II

Non electrical parameters, flow of feed water, fuel, air and steam with correction factors for temperature – Pressure – temperature – level radiation detectors – smoke density measurements – dust monitor.

UNIT – IV: COMBUSTION CONTROL IN BOILERS

Combustion control – control of Main header Pressure, air fuel ratio control – furnace draft and excessive air control, drum level (three element control) main and reheat steam temperature control, burner tilting up, bypass damper, super heater

UNIT – V: OTHER CONTROLS

Spray and gas recirculation controls – BFP recirculation control – Hot well and deaerator level control – pulverizer control, Computers in Power Plants.

UNIT – VI: TURBINE MONITORING AND CONTROL

Condenser vacuum control – gland steam exhaust pressure control – Speed, vibration, Shell temperature monitoring and control – Lubricating oil temperature control – Hydrogen – generator cooling system.

UNIT – VII: ANALYZERS IN POWER PLANTS - I

Thermal conductive type – paramagnetic type, Oxygen analyzer, infrared type and trim analyzer – Spectrum analyzer – hydrogen purity meter

UNIT – VIII: ANALYZERS IN POWER PLANTS – II Chromatography – pH meter – Conductivity cell – fuel analyzer, brief survey of pollution monitoring and control equipment.

TEXT BOOKS:

1. Modern Power Stations Practice, vol. 6, Instrumentation, Controls and Testing - Pergamon Press, Oxford, 1971.
2. Power Plant Technology – by Wakil M.M., McGraw Hill.

REFERENCES:

1. Standard Boiler Operations - Questions and Answers – by Elonka S.M., and Kohal A.L., TMH, New Delhi, 1994.

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OPERATING SYSTEMS

UNIT I :

Computer System and Operating System Overview: Overview of computer operating systems operating systems functions protection and security distributed systems special purpose systems operating systems structures and systems calls operating systems generation

UNIT II :

Process Management – Process concepts threads, scheduling-criteria algorithms, their evaluation, Thread scheduling, case studies UNIX, Linux, Windows

UNIT III :

Concurrency : Process synchronization, the critical-section problem, Peterson's Solution, synchronization Hardware, semaphores, classic problems of synchronization, monitors, Synchronization examples, atomic transactions. Case studies UNIX, Linux, Windows

UNIT IV :

Memory Management : Swapping, contiguous memory allocation, paging, structure of the page table , segmentation, virtual memory, demand paging, page-Replacement, algorithms, case studies UNIX, Linux, Windows

UNIT V :

Principles of deadlock – system model, deadlock characterization, deadlock prevention, detection and avoidance, recovery from deadlock,
I/O systems, Hardware, application interface, kernel I/O subsystem, Transforming I/O requests Hardware operation, STREAMS, performance.

UNIT VI :

File system Interface- the concept of a file, Access Methods, Directory structure, File system mounting, file sharing, protection.

File System implementation- File system structure, file system implementation, directory implementation, directory implementation, allocation methods, free-space management, efficiency and performance, case studies. UNIX, Linux, Windows

UNIT VII :

Mass-storage structure overview of Mass-storage structure, Disk structure, disk attachment disk scheduling, swap-space management, RAID structure, stable-storage implementation, Tertiary storage structure.

UNIT VIII :

Protection : Protection, Goals of Protection, Principles of Protection, Domain of protection Access Matrix, Implementation of Access Matrix, Access control, Revocation of Access Rights, Capability- Based systems, Language – Based Protection,

Security- The Security problem, program threats, system and network threats cryptography as a security tool, user authentication, implementing security defenses, firewalling to protect systems and networks, computer –security classifications, case studies UNIX, Linux, Windows

TEXT BOOKS :

1. Operating System Concepts- Abraham Silberchatz, Peter B. Galvin, Greg Gagne 7th Edition, John Wiley.
2. Operating systems- A Concept based Approach-D.M.Dhamdhare, 2nd Edition, TMH

REFERENCES :

1. Operating Systems' – Internal and Design Principles Stallings, Fifth Edition–2005, Pearson education/PHI
2. Operating System A Design Approach-Crowley, TMH.
3. Modern Operating Systems, Andrew S Tanenbaum 2nd edition Pearson/PHI.

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**VIRTUAL INSTRUMENTATION
(ELECTIVE - I)**

UNIT –I

Virtual Instrumentation: Historical perspective, advantages, block diagram and architecture of a virtual instrument, data-flow techniques, graphical programming in data flow, comparison with conventional programming. Development of Virtual Instrument using GUI, Real-time systems, Embedded Controller, OPC, HMI / SCADA software, Active X programming.

UNIT – II

VI programming techniques: VIS and sub-VIS, loops and charts, arrays, clusters and graphs, case and sequence structures, formula nodes, local and global variables, string and file I/O, Instrument Drivers, Publishing measurement data in the web.

UNIT –III

Data acquisition basics: Introduction to data acquisition on PC, Sampling fundamentals, Input/ Output techniques and buses. ADC, DAC, Digital I/O, counters and timers, DMA, Software and hardware installation, Calibration, Resolution, Data acquisition interface requirements.

UNIT –IV

VI Chassis requirements. Common Instrument Interfaces: Current loop, RS 232C/ RS485, GPIB.

UNIT –V

Bus Interfaces: USB, PCMCIA, VXI, SCSI, PCI, PXI, Firewire. PXI system controllers, Ethernet control of PXI.

UNIT –VI

Networking basics for office & Industrial applications, VISA and IVI.

UNIT – VII

VI toolsets, Distributed I/O modules. Application of Virtual Instrumentation: Instrument Control, Development of process database management system

UNIT –VIII

Simulation of systems using VI, Development of Control system, Industrial Communication, Image acquisition and processing, Motion control.

TEXT BOOKS:

1. Gary Johnson, LabVIEW Graphical Programming, 2nd edition, McGraw Hill, Newyork, 1997.
2. Lisa K. wells & Jeffrey Travis, LabVIEW for everyone, Prentice Hall, New Jersey, 1997.

REFERENCES:

1. Kevin James, PC Interfacing and Data Acquisition: Techniques for Measurement, Instrumentation and Control, Newnes, 2000.

Course Aim: This course aims to introduce the latest instrumentation system design and development tools available today

Prerequisite: Course on personal computer systems and interfacing

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**DIGITAL CONTROL SYSTEMS
(ELECTIVE - II)**

UNIT – I SAMPLING AND RECONSTRUCTION

Introduction, Examples of Data control systems – Digital to Analog conversion and Analog to Digital conversion, sample and hold operations.

UNIT-II THE Z – TRANSFORMS

Introduction, Linear difference equations, pulse response, Z – transforms, Theorems of Z – Transforms, the inverse Z – transforms, Modified Z- Transforms

UNIT-III Z-PLANE ANALYSIS OF DISCRETE-TIME CONTROL SYSTEM

Z-Transform method for solving difference equations; Pulse transforms function, block diagram analysis of sampled – data systems, mapping between s-plane and z-plane.

UNIT – IV STATE SPACE ANALYSIS

State Space Representation of discrete time systems, Pulse Transfer Function Matrix solving discrete time state space equations, State transition matrix and it's Properties, Methods for Computation of State Transition Matrix, Discretization of continuous time state – space equations

UNIT – V CONTROLLABILITY AND OBSERVABILITY

Concepts of Controllability and Observability, Tests for controllability and Observability. Duality between Controllability and Observability, Controllability and Observability conditions for Pulse Transfer Function

UNIT – VI STABILITY ANALYSIS

Mapping between the S-Plane and the Z-Plane – Primary strips and Complementary Strips – Constant frequency loci, Constant damping ratio loci, Stability Analysis of closed loop systems in the Z-Plane. Jury stability test – Stability Analysis by use of the Bilinear Transformation and Routh Stability criterion.

UNIT – VII DESIGN OF DISCRETE TIME CONTROL SYSTEM BY CONVENTIONAL METHODS

Transient and steady – State response Analysis – Design based on the frequency response method – Bilinear Transformation and Design procedure in the w-plane, Lead, Lag and Lead-Lag compensators and digital PID controllers.

UNIT – VIII STATE FEEDBACK CONTROLLERS AND OBSERVERS

Design of state feedback controller through pole placement – Necessary and sufficient conditions, Ackerman's formula.

State Observers – Full order and Reduced order observers.

TEXT BOOKS:

1. Discrete-Time Control systems - K. Ogata, Pearson Education/PHI, 2nd Edition
2. Digital Control and State Variable Methods by M.Gopal, TMH

REFERENCES:

1. Digital Control Systems, Kuo, Oxford University Press, 2nd Edition, 2003.
2. Digital Control Engineering, M.Gopal

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**ARTIFICIAL NEURAL NETWORKS
(ELECTIVE - II)**

UNIT I

INTRODUCTION TO ARTIFICIAL NEURAL NETWORKS : Introduction, Artificial Neural Networks, Historical Development of Neural Networks, Biological Neural Networks, Comparison Between Brain and the Computer, Comparison Between Artificial and Biological Neural Networks, Network Architecture, Setting the Weights, Activation Functions, Learning Methods.

UNIT II

FUNDAMENTAL MODELS OF ARTIFICIAL NEURAL NETWORKS : Introduction, McCulloch – Pitts Neuron Model, Architecture, Learning Rules, Hebbian Learning Rule, Perceptron Learning Rule, Delta Learning Rule (Widrow-Hoff Rule or Leastmean Square (LMS) rule, Competitive Learning Rule, Out Star Learning Rule, Boltzmann Learning, Memory Based Learning.

UNIT III

FEED FORWARD NETWORKS : Introduction, Single Layer Perceptron Architecture, Algorithm, Application Procedure, Perception Algorithm for Several Output Classes, Perceptron Convergence Theorem, Brief Introduction to Multilayer Perceptron networks, Back Propagation Network (BPN), Generalized Delta Learning Rule, Back Propagation rule, Architecture, Training Algorithm, Selection of Parameters, Learning in Back Propagation, Application Algorithm, Local Minima and Global Minima, Merits and Demerits of Back Propagation Network, Applications, Radial Basis Function Network (RBFN), Architecture, Training Algorithm for an RBFN with Fixed Centers.

UNIT IV

ADALINE AND MADALINE NETWORKS : Introduction, Adaline Architecture, Algorithm, Applications, Madaline, Architecture, MRI Algorithm, MRII Algorithm.

UNIT V

COUNTER PROPAGATION NETWORKS : Winner Take – all learning, out star learning, Kohonen Self organizing network, Grossberg layer Network, Full Counter Propagation Network (Full CPN), Architecture, Training Phases of Full CPN, Training Algorithm, Application Procedure, Forward Only counter Propagation Network, Architecture, Training Algorithm, Applications, Learning Vector Quantizer (LVQ).

UNIT VI

ASSOCIATIVE MEMORY NETWORKS - I : Types, Architecture, Continuous and Discrete Hopfield Networks, Energy Analysis, Storage and Retrieval Algorithms, Problems with Hopfield Networks.

UNIT VII

ASSOCIATIVE MEMORY NETWORKS – II : Boltzman Machine, Bidirectional Associative Memory, Adaptive Resonance Theory Networks Introduction, Architecture, Algorithm.

UNIT VIII

APPLICATIONS OF NEURAL NETWORKS : Implementation of A/D Converter using Hopfield Network, Solving Optimization Problems, Solving Simultaneous Linear Equation, Solving Traveling Salesman Problems using Hopfield Networks, Application in Pattern Recognition, Image Processing.

TEXTBOOKS :

1. Introduction to Artificial Neural Systems - J.M.Zurada, Jaico Publishers, 3rd Edition.
2. Introduction to Neural Networks Using MATLAB 6.0 - S.N. Shivanandam, S. Sumati, S. N. Deepa, TMH.

REFERENCES :

1. Elements of Artificial Neural Networks - Kishan Mehrotra, Chelkuri K. Mohan, and Sanjay Ranka, Penram International.
2. Artificial Neural Network – Simon Haykin, Pearson Education, 2nd Ed.
3. Fundamental of Neural Networks – Laurene Fausett, Pearson, 1st Ed.
4. Artificial Neural Networks - B. Yegnanarayana, PHI.

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**COMPUTER NETWORKS
(ELECTIVE - II)**

UNIT – I

Introduction : OSI, TCP/IP and other networks models, Examples of Networks: Novell Networks ,Arpanet, Internet, Network Topologies WAN, LAN, MAN.

UNIT - II

Physical Layer : Transmission media copper, twisted pair wireless, switching and encoding asynchronous communications; Narrow band, broad band ISDN and ATM.

UNIT - III

Data link layer : Design issues, framing, error detection and correction, CRC, Elementary Protocol-stop and wait, Sliding Window, Slip, Data link layer in HDLC, Internet, ATM.

UNIT - IV

Medium Access sub layer : ALOHA, MAC addresses, Carrier sense multiple access. IEEE 802.X Standard Ethernet, wireless LANS. Bridges

UNIT - V

Network Layer : Virtual circuit and Datagram subnets-Routing algorithm shortest path routing, Flooding, Hierarchical routing, Broad cast, Multi cast, distance vector routing.

UNIT – VI

Dynamic routing – Broadcast routing. Rotary for mobility. Congestion, Control Algorithms – General Principles – of Congestion prevention policies. Internet working: The Network layer in the internet and in the ATM Networks.

UNIT –VII

Transport Layer: Transport Services, Connection management, TCP and UDP protocols; ATM AAL Layer Protocol.

UNIT – VIII

Application Layer – Network Security, Domain name system, SNMP, Electronic Mail; the World WEB, Multi Media.

TEXT BOOKS :

1. Computer Networks — Andrew S Tanenbaum, 4th Edition. Pearson Education/PHI
2. Data Communications and Networking – Behrouz A. Forouzan.Third Edition TMH.

REFERENCES :

1. An Engineering Approach to Computer Networks-S.Keshav, 2nd Edition, Pearson Education
2. Understanding communications and Networks, 3rd Edition, W.A. Shay, Thomson

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MICROPROCESSORS AND INTERFACING LAB

I. Microprocessor 8086 :

1. Introduction to MASM/TASM.
2. Arithmetic operation – Multi byte Addition and Subtraction, Multiplication and Division – Signed and unsigned Arithmetic operation, ASCII – arithmetic operation.
3. Logic operations – Shift and rotate – Converting packed BCD to unpacked BCD, BCD to ASCII conversion.
4. By using string operation and Instruction prefix: Move Block, Reverse string, Sorting, Inserting, Deleting, Length of the string, String comparison.
5. DOS/BIOS programming: Reading keyboard (Buffered with and without echo) – Display characters, Strings.

II. Interfacing :

1. 8259 – Interrupt Controller : Generate an interrupt using 8259 timer.
2. 8279 – Keyboard Display : Write a small program to display a string of characters.
3. 8255 – PPI : Write ALP to generate sinusoidal wave using PPI.
4. 8251 – USART : Write a program in ALP to establish Communication between two processors.

III. Microcontroller 8051

1. Reading and Writing on a parallel port.
2. Timer in different modes.
3. Serial communication implementation.

Equipment required for Laboratories:

1. 8086 μ P Kits
2. 8051 Micro Controller kits
3. Interfaces/peripheral subsystems
 - i) 8259 PIC
 - ii) 8279-KB/Display
 - iii) 8255 PPI
 - iv) 8251 USART
4. ADC Interface
5. DAC Interface
6. Traffic Controller Interface
7. Elevator Interface

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INSTRUMENTATION LAB – III

1. Gas analyzer.
2. Gas and liquid chromatography.
3. Spectrometer: UV and VIS spectrometer.
4. Spectrometer: IR and FT IR Spectrometer.
5. Flame photometer.
6. Measurement of calorific value.
7. Mass spectrometer.
8. Interfacing of ADC to PC and observe the data.
9. Interfacing of DAC to PC and generate various types of signals.
10. Serial communication through RS-232C between μ Cs / PCs.
11. GPIB interface – master to slave data transfer.
12. GPIB interface – slave to slave data transfer.
13. Data transfer through IEEE-1394 (fireware) interface.

Equipment:

Gas/ Liquid chromatographer, Gas Analyzer, UV & VIS spectrometer, IR spectrophotometer, Absorption spectrophotometer, Flame photometer, Bomb calorimeter, ADC, DAC add-on cards for PC. GPIB interface Master / slave cards for PC, IEEE-1394 (fireware) interface for PC