

SREE VIDYANIKETHAN ENGINEERING COLLEGE
SREE SAINATH NAGAR, A.RANGAMPET - 517 102

2010-2011

TIME – TABLE

INFORMATION TECHNOLOGY

II B. TECH (I SEMESTER), SECTION -B

Room No. 222

With effect from: 01-07-2010

HOUR DAY	10.30 AM to 11.20 AM 1	11.20 AM to 12.10 PM 2	12.10 PM to 1.00 PM 3	L U N C H	2.15 PM to 3.05 PM 4	3.05 PM to 3.55 PM 5	3.55 PM to 4.45 PM 6
MON	ADS PRD	DLD & CO PB	BEE RKP		DCS TKM	MFCS NS	EDC CVCV
TUE	DLD & CO PB	BEE RKP	EDC CVCV		ADS PRD	DCS TKM	MFCS NS
WED	ADS PRD	EDC CVCV	MFCS NS		DS LAB F&G Lab PRD, VLN,VJ		
THU	BEE RKP	EDC CVCV	MFCS NS		DLD & CO PB	ADS PRD	DCS TKM
FRI	MFCS NS	DCS TKM	DLD & CO PB		EDC CVCV	BEE RKP	ADS PRD
SAT	EE LAB CVCV,RKP				DCS TKM	DLD & CO PB	BEE RKP

ADS : Advanced Data Structures
MFCS : Mathematical Foundations of Computer Science
DLD & CO : Digital Logic Design & Computer Organization
DCS : Data Communication Systems
BEE : Basic Electrical Engineering
EDC : Electronic Devices and Circuits
DS LAB : Data Structures Lab

EE LAB : Electrical and Electronics Lab

PRD : Ms. P. Rama Devi
NS : Ms. N. Sindhuri
PB : Mr. P. Basha
TKM : Mr. T. Krishna Murthy
RKP : Ms. R. Krishna Priya
CVCV : Mr. C. V. Chaitanya Varma
PRD : Ms. P. Rama Devi
VLN : Mr. V. Lokanadham Naidu
VJ : Ms. V. Jyothsna
CVCV : Mr. C. V. Chaitanya Varma
RKP : Ms. R. Krishna Priya

Time Table I/C

HEAD (IT)

PRINCIPAL