

SREE VIDYANIKETHAN ENGINEERING COLLEGE

Sree Sainath Nagar, A.Rangampet – 517 102

TIME TABLE

2010 – 2011

ELECTRICAL & ELECTRONICS ENGINEERING IV – B.TECH-B(I SEMESTER)

Room No: 114

With effect from:01-07-2010

Hour	8:00-8:50	8:50-9:40	9:40-10:30		11:00-11:50	11:50-12:40	12:40-1:30
Day	1	2	3		B	4	5
MON	HVDCT	PSOC	PSD	R	EM/ μ P LAB		
TUE	PSOC	PSD	NN&FL		A	PSA	REAPS
WED	PSA	REAPS	PSD	E	HVDCT	PSOC	NN&FL
THU	HVDCT	PSA	PSOC	A	PSD	NN&FL	REAPS
FRI	NN&FL	HVDCT	PSA	A	REAPS	PSD	PSOC
SAT	μ P/EM LAB			K	NN&FL	PSA	REAPS

NNFL	: Neural Networks & Fuzzy Logic	Prof. M.S. Sujatha
PSD	: Power Semiconductor Drives	Prof. P. Umapathi Reddy
PSA	: Power System Analysis	Mr. E. Kiran Kumar
PSOC	: Power Systems Operation & Control	Ms. P. Hymavathi
REAPS	: Reliability Engineering & Applications to Power Systems	Ms. S.B. Aruna
HVDCT	: HVDC Transmission	Mr. B. Eswaraiah
μ P& μ C Lab	: Microprocessors and Microcontrollers Lab	Ms. P. Hymavathi Mr. P. Ramesh / Mr. E. Kiran Kumar
EM Lab	: Electrical Measurements Lab	Ms. S.B. Aruna Ms. W.V. Jahnvi/ Mr. B. Eswaraiah

Date: 01-07-10

HEAD

PRINCIPAL

Batch 1: **07121A0276 – 2B2**

Batch 2: **07121A02B3 -**

Copy to:

1. The Principal's Office
2. The Examination Cell
3. HOD EEE