

# SREE VIDYANIKETHAN ENGINEERING COLLEGE

Sree Sainath Nagar, A.Rangampet – 517 102

## TIME TABLE

2010 – 2011

### ELECTRICAL & ELECTRONICS ENGINEERING IV – B.TECH-A(I SEMESTER)

Room No: 106

With effect from: 01-07-2010

Hour	8:00-8:50	8:50-9:40	9:40-10:30	B	11:00-11:50	11:50-12:40	12:40-1:30
Day	1	2	3		4	5	6
MON	HVDCT	REAPS	NN&FL	R	PSOC	PSD	PSA
TUE	EM/ $\mu$ P LAB				PSD	HVDCT	NN&FL
WED	NN&FL	HVDCT	PSOC	E	REAPS	PSA	PSD
THU	REAPS	PSOC	PSA		$\mu$ P/EM LAB		
FRI	PSOC	PSA	PSD	A	NN&FL	HVDCT	REAPS
SAT	PSD	NN&FL	PSA		K	REAPS	PSOC

NNFL	: Neural Networks & Fuzzy Logic	Prof. M.S. Sujatha
PSD	: Power Semiconductor Drives	Prof. P. Umamathi Reddy
PSA	: Power System Analysis	Mr. N.M.G. Kumar
PSOC	: Power Systems Operation & Control	Prof. T. Nageswara Prasad
REAPS	: Reliability Engineering & Applications to Power Systems	Ms. S.B. Aruna
HVDCT	: HVDC Transmission	Mr. B. Subba Reddy
$\mu$ P& $\mu$ C Lab	: Microprocessors and Microcontrollers Lab	Mr. A. Munisankar Mr. E. Kiran Kumar
EM Lab	: Electrical Measurements Lab	Mr. N.M.G. Kumar Mr. B. Eswaraiah/ Ms. W.V. Jahnvi
<p><b>Date: 01-07-10</b> <span style="margin-left: 150px;"><b>HEAD</b></span> <span style="float: right;"><b>PRINCIPAL</b></span></p>		

Batch 1: **07121A0201 – 237**

Batch 2: **07121A0238 - 275**

**Copy to:**

1. The Principal's Office
2. The Examination Cell
3. HOD EEE