

SREE VIDYANIKETHAN ENGINEERING COLLEGE

Sree Sainath Nagar, A.Rangampet – 517 102

TIME TABLE

2010 – 2011

ELECTRICAL & ELECTRONICS ENGINEERING II – B.TECH. SECTION – A (I SEMESTER)

Room No: 205

With effect from:01-07-2010

Hour	10:30-11:20	11:20-12:10	12:10-1:00		2:15-3:05	3:05-3:55	3:55-4:45
Day	1	2	3	B	4	5	6
MON	EM-I	EDC	ES	R	EC	FM&HM	M-III
TUE	EDC	ES	EM-I		M-III	EC	FM&HM
WED	ES	EDC	M-III	E	EDC/BFMHM Lab		
THU	M-III	EM-I	FM&HM	A	ES	EDC	EC
FRI	BFMHM/ EDC Lab			K	FM&HM	EC	EM-I
SAT	ES	FM&HM	EC		EM-I	M-III	EDC

M-III	:	Mathematics –III	Dr. M. Sudheer Babu
FM&HM	:	Fluid Mechanics & Hydraulic Machinery	Mr. D. Raghurami Reddy
ES	:	Environmental Science	Mr. B. Mohana Reddy
EDC	:	Electronic Devices & Circuits	Ms. V. Rajani
EC	:	Electrical Circuits	Mr. A. Munisankar
EM-I	:	Electrical Machines –I	Ms. W.V. Jahnavi
BFM&HM LAB	:	Basic Fluid Mechanics & Hydraulic Machinery Lab	Mr. D. Raghurami Reddy Mr. C. Kiran
EDC LAB	:	Electronic Devices & Circuits Lab	Ms. V. Rajani Ms. P. Kavitha

Date: 01-07-10

HEAD

PRINCIPAL

Batch 1: 09121A0201 – 235

Batch 2: 09121A0236 – 270

Copy to:

1. The Principal's Office
2. The Examination Cell
3. HOD EEE