

SREE VIDYANIKETHAN ENGINEERING COLLEGE

Sree Sainath Nagar, A.Rangampet – 517 102

TIME TABLE

2010 – 2011

ELECTRICAL & ELECTRONICS ENGINEERING III – B.TECH., SECTION-B (I- SEMESTER)

Room No: 121

With effect from:01-07-2010

Hour	8:00-8:50	8:50-9:40	9:40-10:30		11:00-11:50	11:50-12:40	12:40-1:30
Day	1	2	3	B	4	5	6
MON	PE	PS-II	CSO	R	EM	LSA	EM-III
TUE	LSA	EM-III	PE		CSO	EM	PS-II
WED	PS-II	LSA	EM	E	EM-II / CS LAB		
THU	CS / EM-II LAB			A	EM-III	CSO	PE
FRI	EM-III	CSO	PS-II		PE	LSA	EM
SAT	EM	PS-II	PE	K	EM-III	CSO	LSA

CSO	: Computer Systems Organization	Dr. K. Ramani
PS – II	: Power system – II	Mr. D. Sreenivasulu Reddy
EM – III	: Electrical Machines –III	Mr. M. Manohara
EM	: Electrical Measurements	Mr. N.M.G. Kumar
PE	: Power Electronics	Ms. R. Krishna Priya
LSA	: Linear System Analysis	Mr. M. Saravanan
EM – II LAB	: Electrical Machines – II Lab	Mr. Manohara Mr. B. Subba Reddy
CS&S LAB	: Control Systems & Simulation Lab	Ms. R. Krishna Priya Mr. D. Sreenivasulu Reddy

Date: 01-07-10

HEAD

PRINCIPAL

Batch 1: 08121A0276 – 2A9

Batch 2: 08121A02B0 –

Copy to:

1. The Principal's Office
2. The Examination Cell
3. HOD EEE