

SREE VIDYANIKETHAN ENGINEERING COLLEGE

Sree Sainath Nagar, A. Rangampet.

DEPARTMENT OF ELECTRONICS AND CONTROL ENGINEERING

TIME TABLE

2010–2011

IV B.TECH. I SEMESTER

Room No: 323

w.e.f: 01-07-2010

Hour	8.00 To 8.50	8.50 To 9.40	9.40 To 10.30	L U N C H B R E A K	11.00 To 11.50	11.50 To 12.40	12.40 To 1.30	
MON	CS-II / JAVA LAB					DCS	ACS	JAVA^T
TUE	NNFL	ES	RA			JAVA/CS-II LAB		
WED	ACS	NNFL	DCS			JAVA	RA	ES^T
THU	DCS	ACS	NNFL			ES	JAVA	RA^T
FRI	RA	JAVA	ACS			DCS	ES	NNFL^T
SAT	ES	ACS^T	NNFL			RA	JAVA	DCS^T

OOPS : OOPS Through JAVA

KCB : Mr.K.Chitti Babu

NNFL : Neural Networks & Fuzzy Logic

KR : Mr.K.Rohith

DCS : Digital Control System

PHK : Mr.P.Hemendra Kumar

ACS : Adaptive Control system

NG : Prof.N.Gireesh

RA : Robotics and Automation

NAK : Mr.N.Anantha Krishna

ES : Embedded Systems

NKR : Mr.Narendra Kumar Rao

JAVA Lab : JAVA Lab

KCB : Mr.K.Chitti Babu

CS-II Lab : Control System Lab -II

PHK : Mr.P.Hemendra Kumar

()^T : Tutorial

HOD

PRINCIPAL