

SREE VIDYANIKETHAN ENGINEERING COLLEGE

Sree Sainath Nagar, A. Rangampet.

DEPARTMENT OF ELECTRONICS AND CONTROL ENGINEERING

TIME TABLE

2010–2011

III B.TECH. I SEMESTER

Room No: 322

w.e.f: 01-07-2010

Hour	8.00 To 8.50	8.50 To 9.40	9.40 To 10.30		11.00 To 11.50	11.50 To 12.40	12.40 To 1.30
MON	PE	LDIC	POC	L U N C H B R E A K	CO	PMMC	PC ^T
TUE	CO	POC	PC		LDIC	PMMC	PE ^T
WED	IC/AECS LAB				PE	PC	POC ^T
THU	PMMC	LDIC	CO		AECS / IC LAB		
FRI	LDIC	PMMC	POC		PC	PE	CO ^T
SAT	POC	PE	CO		LDIC ^T	PC	PMMC ^T

CO : Computer Organization

KCB : Ms.K.Chitti Babu

PMMC : Prime Movers & Mechanical Components

KCI : Mr.K.C.Ijaiah

POC : Principles of Communication

KSC : Mr.K.S.Chakradhar

PE : Power Electronics

NGP : Mr N.Guru Prasad

LDICA : Linear & Digital IC Applications

SV : Ms.S.Vani

PC : Process Control

KR : Mr.K.Rohith

ICA LAB : IC Applications Lab

SV : Ms.S.Vani

AECS Lab : Advanced English Communication Skills Lab

KR : Mr.Ramachandra babu

()^T : Tutorial

HOD

PRINCIPAL