

SREE VIDYANIKETHAN ENGINEERING COLLEGE

SREE SAINATH NAGAR, A. RANGAMPET.

DEPARTMENT OF ELECTRONICS AND CONTROL ENGINEERING

TIME TABLE

2010–2011

II B.TECH. I SEMESTER

Room No: 321

w.e.f:01-07-2010

Hour/ Day	10.30 To 11.20	11.20 To 12.10	12.10 To 1.00		2.15 To 3.05	3.05 To 3.55	3.55 To 4.45
MON	EDC LAB			L U N C H B R E A K	M-III	ES	SS^T
TUE	M-III	EDC	PTSP^T		SS	EC	ES^T
WED	EDC	SS	ES		PTSP	M-III	EC^T
THU	EC	EDC	PTSP		BS LAB		
FRI	PTSP	SS	M-III^T		ES	EC	EDC^T
SAT	EC	M-III	SS		EDC	ES	PTSP

M-III : Mathematics-III	MMR : Mr.M.Mohan Ramana
ES : Environmental Science	MR : Mr. Mohan Reddy
EC : Electrical Circuits	PHK : Mr.P.Hemendra Kumar
PTSP : Probability Theory and stochastic Process	LR : Mr. Lokesh Reddy
EDC : Electronic Devices and Circuits	KSC : Mr.K.S.Chakradhar
SS : Signals & Systems	CS : Ms.S.Vani
EDC Lab : Electronic Devices and Circuits Lab	KR : Mr.K.S.Chakradhar
BS Lab : Basic Simulation Lab	KSC : Mr.K.Rohith
() ^T : Tutorial	

HOD

PRINCIPAL