

SREE VIDYANIKETHAN ENGINEERING COLLEGE
SREE SAINATH NAGAR, TIRUPATI – 517 102
Department Of Computer Science and Systems Engineering
TIME TABLE
(2009-2010)
III B. TECH II Semester

ROOM NO: **712**

w.e.f. **04.01.2010**

DAY/ HR	8:00-8:50AM 1	8:50- 9:40AM 2	9:40-10:30AM 3		11:00-11:50AM 4	11:50-12:40 PM 5	12:40-1:30 PM 6
MON	ACA	AI	OOAD	B R E A K	CN	MMS	WT
TUE	AI	ACA	WT		CN & UML LAB (CMR , LP, KL, CS)		
WED	OOAD	MMS	CN		WT LAB (RD , AC, PLN, AV)		
THU	ACA	OOAD	WT		CN	AI	MMS
FRI	MMS	WT	OOAD		ACA	CN	AI
SAT	WT	CN	AI		ACA	MMS	OOAD

CN	COMPUTER NETWORKS	PROF. C. MADHUSUDHANA RAO
AI	ARTIFICIAL INTELIGENCE	MS T. KAVITHA
OOAD	OBJECT ORIENTED ANALYSIS AND DESIGN	MS L. PADMAJA
MMS	MATHEMATICAL MODELING AND SIMULATION	MS P. DHANA LAKSHMI
WT	WEB TECHNOLOGIES	MR. R. DWARAKANATH
ACA	ADVANCED COMPUTER ARCHITECTURE	MR. P. LAKSHMI NARAYANA
CN & UML LAB	COMPUTER NETWORKS AND UML LAB	CMR , LP, KL, CS
WT LAB	WEB TECHNOLOGIES LAB	RD , AC, PLN, AV

HOD

PRINCIPAL