

SREE VIDYANIKETHAN ENGINEERING COLLEGE

SREE SAINATH NAGAR, A. RANGAMPET – 517 102

Department Of Computer Science and Systems Engineering

TIME TABLE

(2010-2011)

IV B. TECH I Semester

ROOM NO: **713**

w.e.f. **01.07.2010**

DAY/ HR	8:00- 8:50 1	8:50- 9:40 2	9:40- 10:30 3		11:00- 11:50 4	11:50- 12:40 5	12:40- 1:30 6		2:15- 3:05 7	3:05- 3:55 8	3:55- 4:45 9
MON	ES	MAD	CSM	L U N C H B R E A K	NN	IS	PECS	L U N C H B R E A K	ES LAB (KH,AC, PLN, KL, TK)		
TUE	MAD	ES	NN		CSM	PECS	IS				
WED	CSM	NN	ES		MAD LAB(A&B) (LP, TK, AC, KL, PDL)						
THU	NN	PECS	IS		MAD	ES	CSM				
FRI					MAD	PECS	IS		ES LAB (KH,AC, PLN, KL, TK)		
SAT	PECS	IS	NN		CSM	MAD	ES				

MAD	MULTIMEDIA AND APPLICATION DEVELOPMENT	MS. L. PADMAJA
ES	EMBEDDED SYSTEMS	MR. K. HAJARATAIAH
PECS	PERFOMANCE EVALUATION OF COMPUTER SYSTEMS	MS. P. DHANALAKSHMI
CSM	COMPUTER SYSTEMS MAINTENANCE	MS. K. LALITHA
IS	INFORMATION SECURITY	MS. T. KAVITHA
NN	NEURAL NETWORKS	Prof C.MADHUSUDHANA RAO
ES LAB	EMBEDDED SYSTEMS LAB	KH,AC, PLN, KL, TK
MADLAB	MULTIMEDIA AND APPLICATION DEVELOPMENT LAB	LP, TK, AC, KL, PDL

HOD

PRINCIPAL