

SREE VIDYANIKETHAN ENGINEERING COLLEGE

SREE SAINATH NAGAR, TIRUPATI

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

TIME TABLE ACADEMIC YEAR 2010-2011 III B.Tech - Semester - I - Section B

Room No. 131

w.e.f. 01-07-2010

HOUR	8:00 8:50	8:50 9:40	9:40 10:30		11:00 11:50	11:50 12:40	12:40 1:30				
DAY					10:30 11:20	11:20 12:10	12:10 1:00		2:15 3:05	3:05 3:55	3:55 4:45
MON	FLAT	DCS	MPI	BREAK	STM	DAA	CG	LUNCH			
TUE	CG	DAA	DCS		FLAT	STM	MPI				
WED	DCS	CG	STM		MPI	FLAT	DAA				
THU					DAA	MPI	DCS		MPI LAB (BATCH -1) / ELCS LAB (BATCH -2)		
FRI	MPI LAB (BATCH -2) / ELCS LAB (BATCH -1)				CG	STM	FLAT				
SAT	FLAT	STM	CG		MPI	DCS	DAA				

FLAT	Formal Languages and Automata Theory	Mr. D. PRAMOD KRISHNA
STM	Software Testing Methodologies	Mr. M. ABHILASH KUMAR
CG	Computer Graphics	Dr. L.V. REDDY
MPI	Microprocessors and Interfacing	Mr. N. PAPANNA
DCS	Data Communication Systems	Ms. H.D. PRAVEENA
DAA	Design and Analysis of Algorithms	Prof. K. DELHI BABU

AECS LAB	Advanced English Communication Skills Lab
MPI LAB	Microprocessors and Interfacing Lab

M. RAVICHAND		
NP	VJG (THU)	
IS (FRI)		

BATCH - 1	08121A0576 - 08121A05B0
BATCH - 2	08121A05B2 - 09125A0511