

SREE VIDYANIKETHAN ENGINEERING COLLEGE

SREE SAINATH NAGAR, TIRUPATI

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

TIME TABLE ACADEMIC YEAR 2010-2011 III B.Tech - Semester - I - Section A

Room No. 128

w.e.f. 01-07-2010

| HOUR | 8:00 | 8:50 | 9:40 | BREAK | 11:00 | 11:50 | 12:40 | LUNCH | w.e.f. 01-07-2010 | | |
|------|---|------|-------|-------|---|-------|-------|-------|-------------------|------|------|
| | 8:50 | 9:40 | 10:30 | | 11:50 | 12:40 | 1:30 | | 2:15 | 3:05 | 3:55 |
| DAY | | | | | 10:30 | 11:20 | 12:10 | | 3:05 | 3:55 | 4:45 |
| MON | MPI LAB (BATCH -1) / ELCS LAB (BATCH -2) | | | | DAA | DCS | FLAT | | | | |
| TUE | DAA | FLAT | CG | | STM | MPI | DCS | | | | |
| WED | MPI | STM | DCS | | FLAT | DAA | CG | | | | |
| THU | FLAT | MPI | STM | | DCS | CG | DAA | | | | |
| FRI | CG | DCS | MPI | | DAA | FLAT | STM | | | | |
| SAT | STM | CG | MPI | | MPI LAB (BATCH -2) / ELCS LAB (BATCH -1) | | | | | | |

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|------|--------------------------------------|------------------------|
| FLAT | Formal Languages and Automata Theory | Mr. K. ANIL KUMAR |
| STM | Software Testing Methodologies | Mr. N. PAPANNA |
| CG | Computer Graphics | Ms. S. SHABANA |
| MPI | Microprocessors and Interfacing | Mr. SHAIK SALAM |
| DCS | Data Communication Systems | Ms. H.D. PRAVEENA |
| DAA | Design and Analysis of Algorithms | Mr. K. MUNIVARA PRASAD |

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| AECS LAB | Advanced English Communication Skills Lab |
| MPI LAB | Microprocessors and Interfacing Lab |

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|---------------|-------|--|--|
| A. | | | |
| RAJESH | | | |
| | KAK | | |
| SS | (SAT) | | |
| ASV | | | |
| (MON) | | | |

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|-----------|-------------------------|
| BATCH - 1 | 08121A0501 - 08121A0538 |
| BATCH - 2 | 08121A0539 - 08121A0575 |