

SREE VIDYANIKETHAN ENGINEERING COLLEGE

SREE SAINATH NAGAR, TIRUPATI

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

SVEC/CSE/02/2010-11

TIME TABLE ACADEMIC YEAR 2010-2011 II B.Tech - Semester - I - Section B

Room No. 134

w.e.f. 01-07-2010

HOUR	8:00	8:50	9:40	BREAK	11:00	11:50	12:40	LUNCH			
	8:50	9:40	10:30		10:30	11:20	12:10		1:30	2:15	3:05
DAY					10:30	11:20	12:10		2:15	3:05	3:55
					11:20	12:10	1:00		3:05	3:55	4:45
MON					DLD	EDC	P & S		BEE	ADS	MFCS
TUE					BEE	MFCS	ADS		EDC	P & S	DLD
WED					EDC	MFCS	DLD		DS LAB (C & D)		
THU					MFCS	ADS	EDC		DLD	BEE	P & S
FRI					ADS	BEE	P & S		EE LAB		
SAT					P & S	DLD	MFCS		ADS	EDC	BEE

P & S	Probability & Statistics	Mr. J. GIREESH KUMAR
BEE	Basic Electrical Engineering	Mr. B. ESWARAI AH
MFCS	Mathematical Foundations of Computer Science	Dr. L.V. REDDY
ADS	Advanced Data Structures	Mr. D. PRAMOD KRISHNA
DLD	Digital Logic Design	Ms. I. SHEEBA
EDC	Electronic Devices and Circuits	Ms. A. SRIVIDYA

EE LAB	Electrical and Electronics Lab
DS LAB	Data Structures Lab

ASV	BE	
DPK	NP	MAK
VJG		