SKILL DEVELOPMENT PROGRAMME

The Department of Computer Science and Systems Engineering organized Three Day Skill Development Programme on “Parallel programming using OpenMP” from 29-11-2013 to 01-12-2013 under TEQIP-II, Finishing School. This Skill Development Programme covered Multicore Architectures, Multi Threading concepts and OpenMP. Mainly focused on OpenMP programming constructs, Runtime Library Routines Scheduling Clause, Programs on Scheduling Clause and Programs on Critical and Reduction Clause. The resource persons demonstrated sample projects on Multicore for the benefit of students.

The students at IV B.Tech level gained knowledge on Multicore Programming and Parallel Programming using OpenMp.

Dr. M. Rajasekhar Babu, VIT, Vellore delivering lecture on “Fundamentals of Multicore Architectures”

Dr. M. Rajasekhar Babu, VIT, Vellore delivering lecture on “OpenMP programming constructs”

Hands on Training on Sample Programs Practice, Projects on MultiCore

Practical session on Scheduling Clauses, Programs on Critical and Reduction Clause